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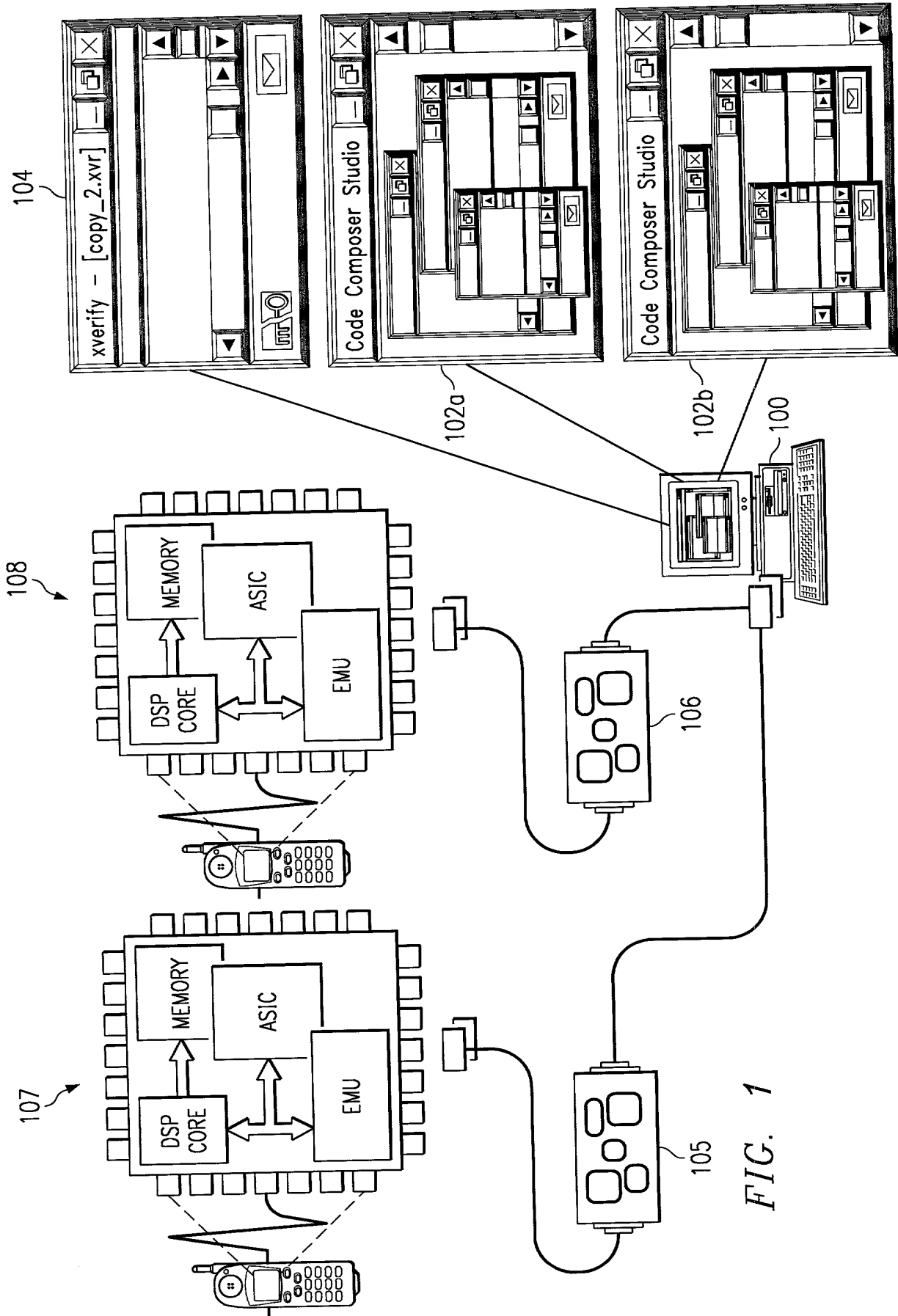


FIG. 1

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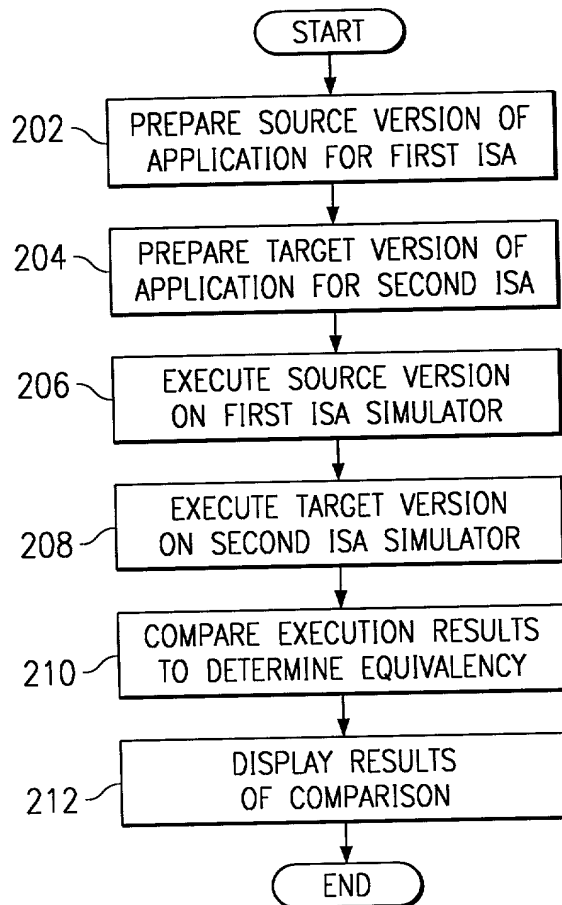


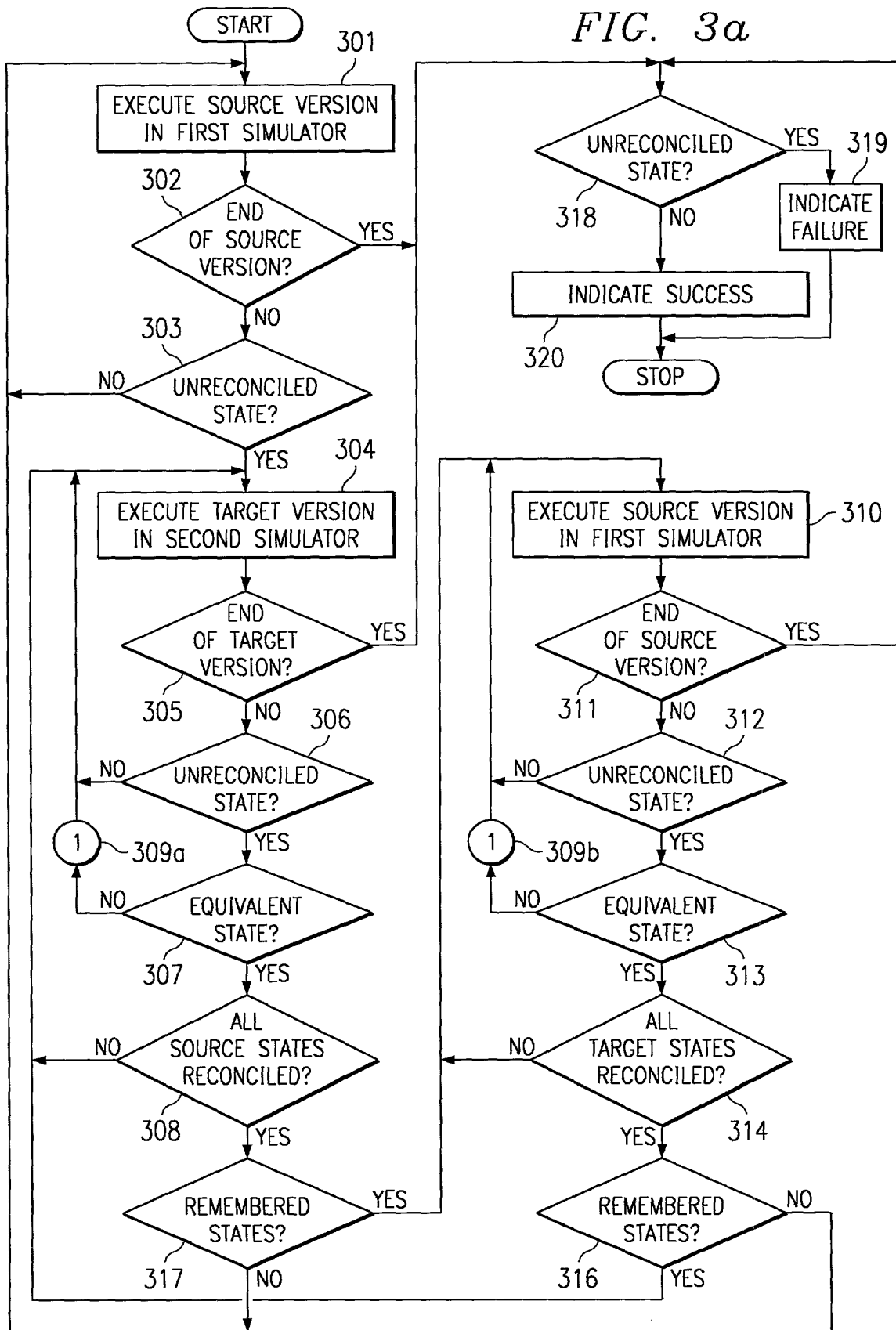
FIG. 2

403 SOURCE EXECUTION	405 SOURCE	404 SOURCE STATE	406 TARGET EXECUTION	404 TARGET	406 TARGET STATE
→	STM data1, AR3	EMPTY	→	MOV data1, AR3	EMPTY
401	LD #1, A		402	MOV #1, AC0	
	NOP			NOP	
	NOP			NOP	
	STL A, *AR3+			MOV AC0<<#0, AR3+	
	NOP			NOP	

FIG. 4a

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FIG. 3a



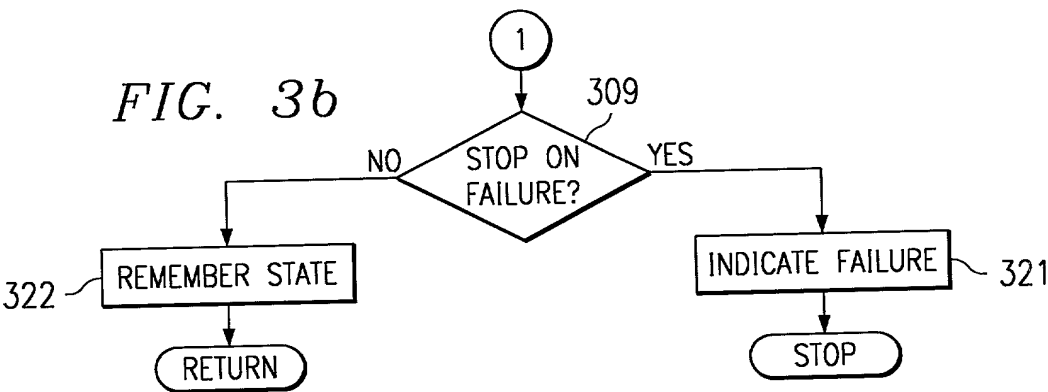


FIG. 4b

SOURCE EXECUTION	SOURCE	SOURCE STATE	TARGET EXECUTION	TARGET	TARGET STATE
	STM data1, AR3	AR3=0x800		MOV data1, AR3	EMPTY
	LD #1, A		402	MOV #1, ACO	
401	NOP			NOP	
	NOP			NOP	
	STL A, *AR3+			MOV ACO<<#0, AR3+	
	NOP			NOP	

FIG. 4c

SOURCE EXECUTION	SOURCE	SOURCE STATE	TARGET EXECUTION	TARGET	TARGET STATE
	STM data1, AR3	AR3=0x800		MOV data1, AR3	XAR3=0x430
	LD #1, A			MOV #1, ACO	
401	NOP		402	NOP	
	NOP			NOP	
	STL A, *AR3+			MOV ACO<<#0, AR3+	
	NOP			NOP	

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	403	405		404	406
SOURCE EXECUTION	SOURCE	SOURCE STATE	TARGET EXECUTION	TARGET	TARGET STATE
	STM data1, AR3	AR3=0x800 A=1		MOV data1, AR3	XAR3=0x430
	LD #1, A			MOV #1, AC0	
→	NOP		→ 402	NOP	
401	NOP			NOP	
	STL A, *AR3+			MOV AC0<<#0, AR3+	
	NOP			NOP	

FIG. 4d

	403	405		404	406
SOURCE EXECUTION	SOURCE	SOURCE STATE	TARGET EXECUTION	TARGET	TARGET STATE
	STM data1, AR3	AR3=0x800 A=1		MOV data1, AR3	XAR3=0x430 AC0=1
	LD #1, A			MOV #1, AC0	
→	NOP		→	NOP	
401	NOP		402	NOP	
	STL A, *AR3+			MOV AC0<<#0, AR3+	
	NOP			NOP	

FIG. 4e

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SOURCE EXECUTION	403 SOURCE	405 SOURCE STATE	TARGET EXECUTION	404 TARGET	406 TARGET STATE
	STM data1, AR3	AR3=0x800 (*AR3)=1		MOV data1, AR3	XAR3=0x430
	LD #1, A			MOV #1, AC0	
	NOP		402 →	NOP	
	NOP			NOP	
401 →	STL A, *AR3+			MOV AC0<<#0, AR3+	
	NOP			NOP	

FIG. 4f

SOURCE EXECUTION	403 SOURCE	405 SOURCE STATE	TARGET EXECUTION	404 TARGET	406 TARGET STATE
	STM data1, AR3	AR3=0x800 (*AR3)=1		MOV data1, AR3	XAR3=0x430 (*XAR3)=1
	LD #1, A			MOV #1, AC0	
	NOP			NOP	
	NOP			NOP	
401 →	STL A, *AR3+		402 →	MOV AC0<<#0, AR3+	
	NOP			NOP	

FIG. 4g

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FIG. 4h

403		405	404		406
SOURCE EXECUTION	SOURCE	SOURCE STATE	TARGET EXECUTION	TARGET	TARGET STATE
	STM data1, AR3	EMPTY		MOV data1, AR3	EMPTY
	LD #1, A			MOV #1, AC0	
	NOP			NOP	
	NOP			NOP	
401	STL A, *AR3+		402	MOV AC0<<#0, AR3+	
→	NOP		→	NOP	

FIG. 4h 262001

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FIG. 5a

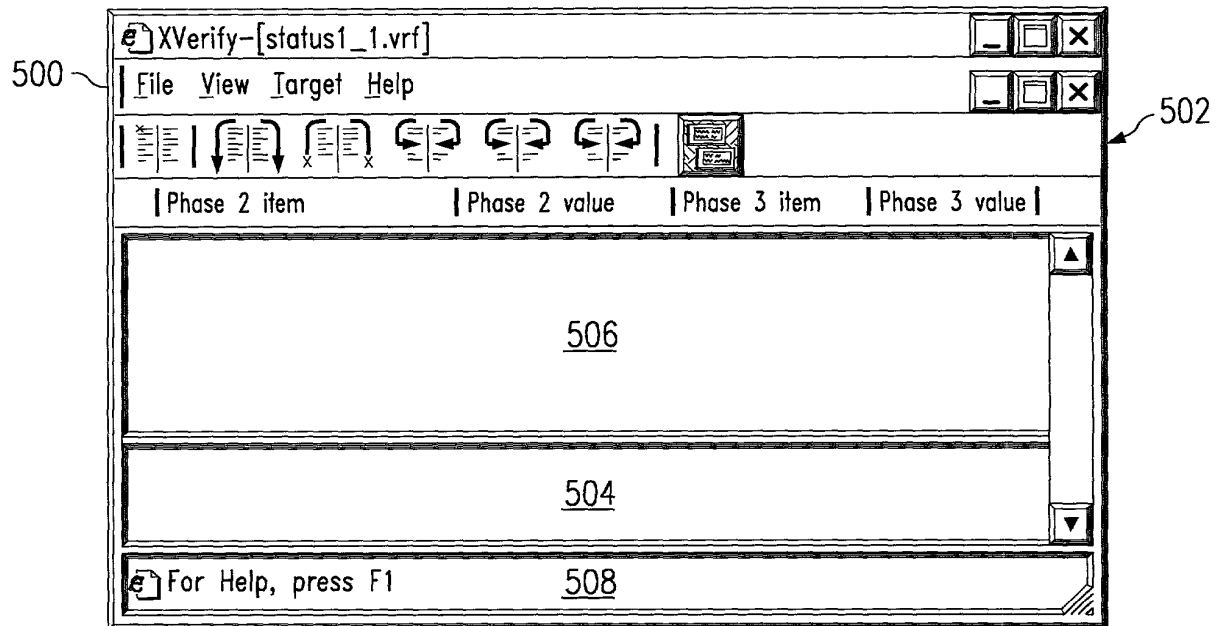


FIG. 5b

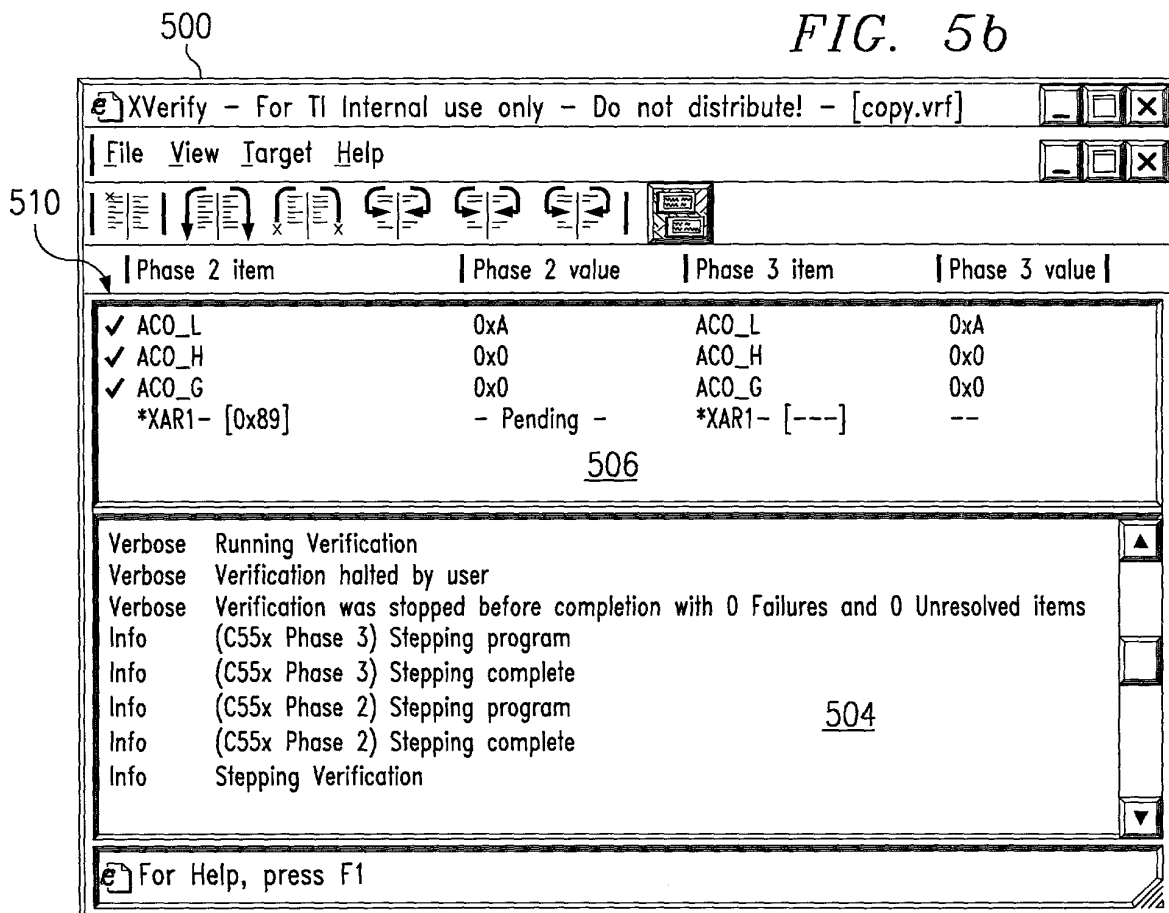




FIG. 6a

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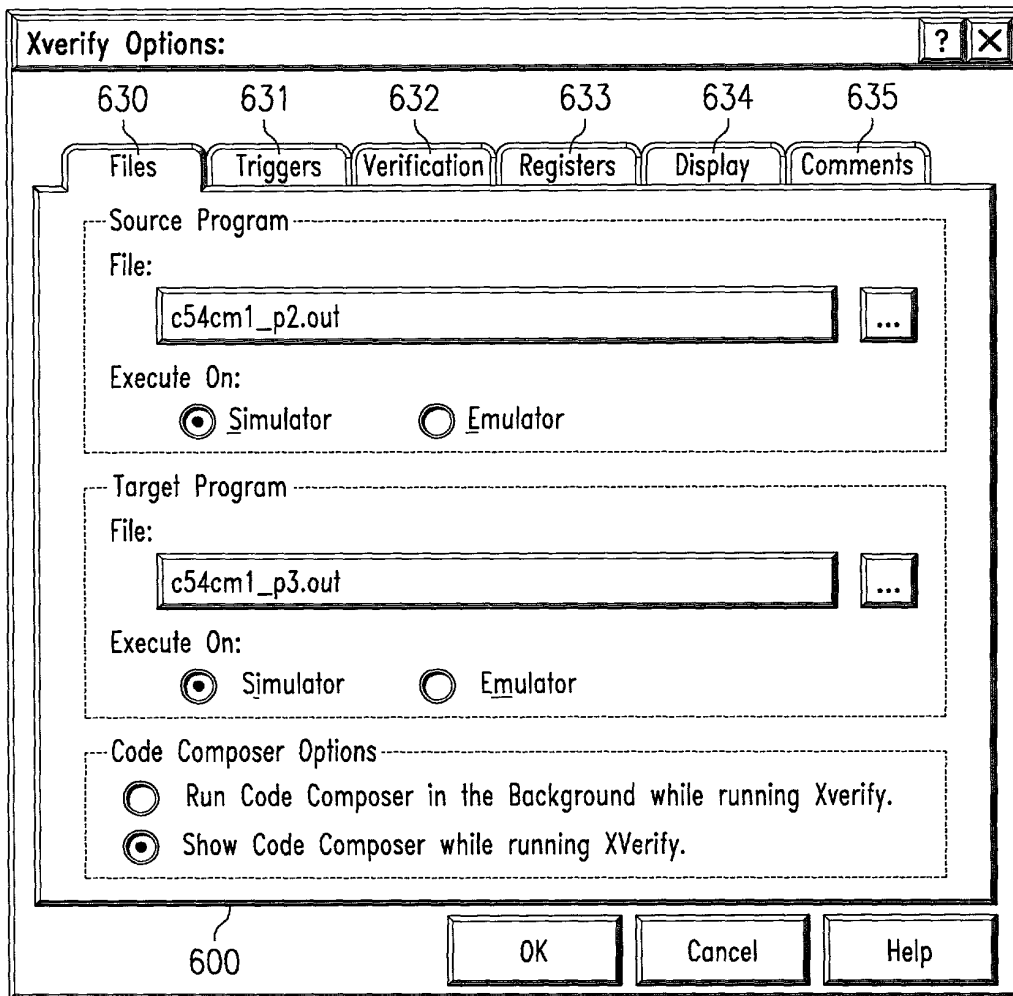
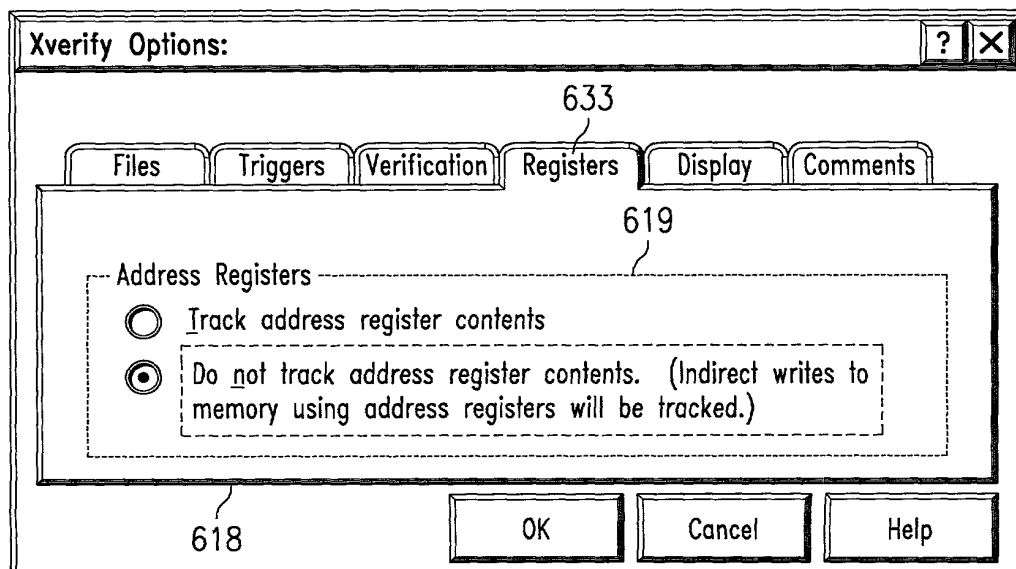


FIG. 6d



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FIG. 6b

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Xverify Options: ? X

Files Triggers Verification Registers Display Comments

Trigger ON points cause verification to begin at that address. Trigger OFF points cause verification to stop and the program to be executed without verification up to the next ON point. Trigger STOP points halt the verification process.

Source Program

☒ Turn on verification ☐ Turn off verification ☐ Stop verification

Add Remove

Address 604

Address	Type
begin_verify	On
end_verify	Stop

608

Target Program

☒ Turn on verification ☐ Turn off verification ☐ Stop verification

Add Remove

Address 606

Address	Type
begin_verify	On
end_verify	Stop

610

602

OK Cancel Help

FIG. 6f

Xverify Options: X

Files Triggers Verification Registers Display Comments 635

Enter any comments for this verification document in the box below.

626

OK Cancel Apply Help

FIG. 6c

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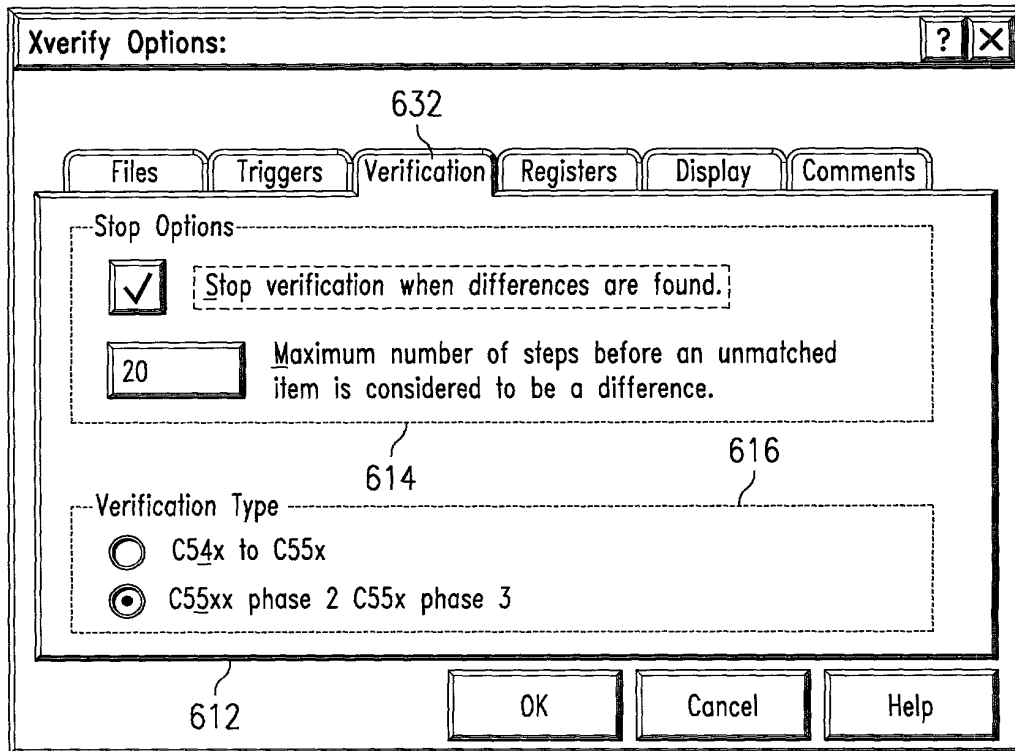
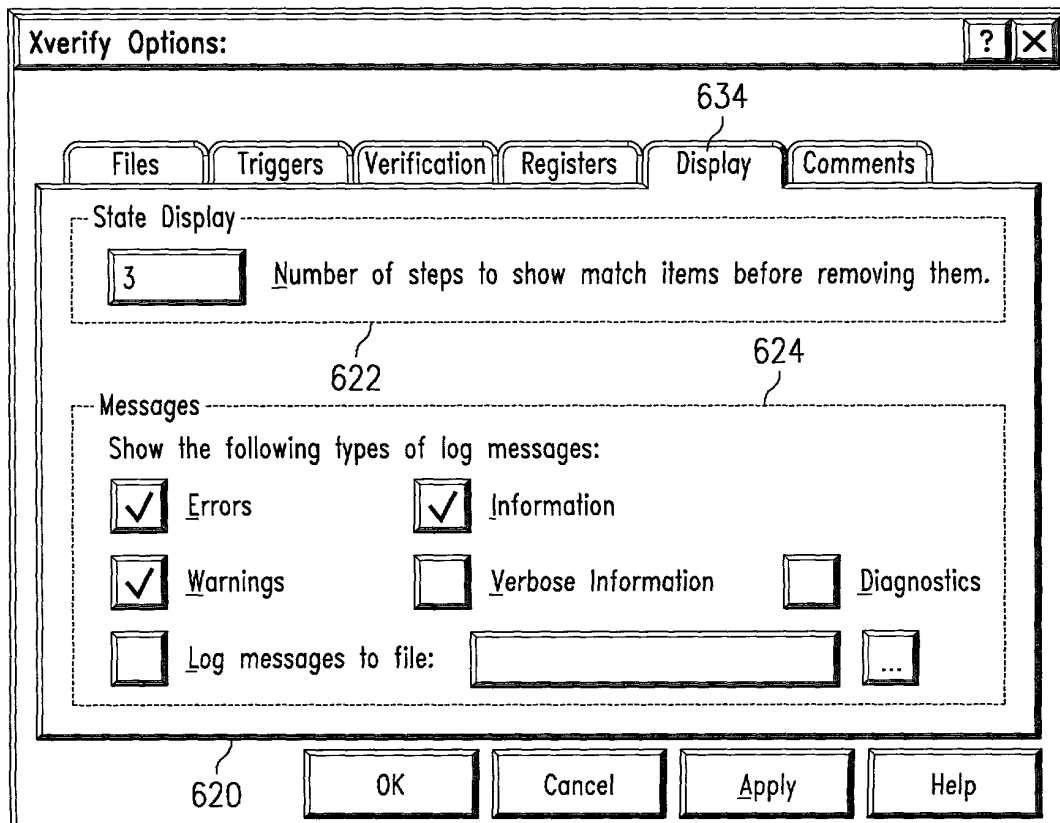


FIG. 6e



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FIG. 7

